

Results for "(((resizing rules for mos analog-design reuse))<in>metadata)"

Your search matched 1 of 2027183 documents.

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

[e-mail](#) [printer friendly](#)

New [Beta]
**Application
Notes**
FORWARDER
GLOBALSPEC

Search Options

[View Session History](#)[New Search](#)

Key

	Open access content
IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

(((resizing rules for mos analog-design reuse))<in>metadata)

[Search](#)☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract

IEEE/ET

Books

Educational Courses

Application Notes [Beta]

IEEE/ET journals, transactions, letters, magazines, conference proceedings, and standards.

[view selected items](#) [Select All](#) [Deselect All](#)

- ☐ 1. **Resizing rules for MOS analog-design reuse**
 Galup-Montoro, C.; Schneider, M.C.; Coitinho, R.M.;
[Design & Test of Computers, IEEE](#)
 Volume 19, [issue 2](#), March-April 2002 Page(s):50 - 58
 Digital Object Identifier 10.1109/54.990442
[AbstractPlus](#) | [Full Text: PDF\(343 KB\)](#) [IEEE JNL](#)
[Rights and Permissions](#)

[Help](#) [Contact Us](#) [Privacy & Security](#) [IEEE.org](#)

 Indexed by
 Inspec®

© Copyright 2006 IEEE - All Rights Reserved